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## 1. Diagonal test and diagnostic schemes for flash memories

Sau-Kwo Chiu; Jen-Chieh Yeh; Chih-Tsun Huang; Cheng-Wen Wu; Test Conference, 2002. Proceedings. International 7-10 Oct. 2002 Page(s):37 - 46

**IEEE CNF** 

## 2. Parallel implementation of a power system dynamic simulation methodology using the conjugate gradient method

Decker, I.C.; Falcao, D.M.; Kaszkurewicz, E.; Power Industry Computer Application Conference, 1991. Conference Proceedings 7-10 May 1991 Page(s):245 - 252

**IEEE CNF** 

## 3. Parallel testing of parametric faults in a three-dimensional dynamic random-access memory

Mazumder, P.; Solid-State Circuits, IEEE Journal of Volume 23, Issue 4, Aug. 1988 Page(s):933 - 941

Time-domain integration of the Maxwell equations on finite elements

Lynch, D.R.; Paulsen, K.D.; Antennas and Propagation, IEEE Transactions on Volume 38, Issue 12, Dec. 1990 Page(s):1933 - 1942 **IEEE JNL** 

5. Parallel implementation of a power system dynamic simulation methodology using the conjugate gradient

Decker, I.C.; Falcao, D.M.; Kaszkurewicz, E.; Power Systems, IEEE Transactions on Volume 7, Issue 1, Feb. 1992 Page(s):458 - 465

**IEEE JNL** 

## 6. Multicoloring of grid-structured PDE solvers on shared-memory multiprocessors

Hwang-Cheng Wang; Kai Hwang; Parallel and Distributed Systems, IEEE Transactions on Volume 6, Issue 11, Nov. 1995 Page(s):1195 - 1205 **IEEE JNL** 

7. On comparing functional fault coverage and defect coverage for memory testing

Von-Kyoung Kim; Chen, T.; Computer-Aided Design of Integrated Circuits and Systems, IEEE Transactions on Volume 18, Issue 11, Nov. 1999 Page(s):1676 - 1683 **IEEE JNL** 

8. Reducing magnetic bearing currents via gain scheduled adaptive control

Betschon, F.; Knospe, C.R.; Mechatronics, IEEE/ASME Transactions on Volume 6, Issue 4, Dec. 2001 Page(s):437 - 443 **IEEE JNL** 

Performance measurement and comparison of a set of parallel periodic and non-periodic tridiagonal solvers Xian-He Sun; Moitra, S.;

Parallel Architectures, Algorithms, and Networks, 1996. Proceedings. Second International Symposium on 12-14 June 1996 Page(s):480 - 486

10. Alpha-particle-induced charge collection measurements for megabit DRAM cells

Takeuchi, K.; Shimohigashi, K.; Takeda, E.; Yamasaki, E.; Toyabe, T.; Itoh, K.; Electron Devices, IEEE Transactions on Volume 36, Issue 9, Sept. 1989 Page(s):1644 - 1650 **IEEE JNL** 

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S10	23129	(diagonal and test)".clm"	US-PGPUB;	OR	ON	2005/10/28 10:47
			USPAT; EPO; JPO; DERWENT; IBM_TDB			
S12	16	(diagonal and test).clm. and (diagonal and test).ab.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/28 02:17
S13	276	(diagonal and (write writing written erase erasing erasure erased)).clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/28 10:51
S14	283	(diagonal and test).clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/28 10:48
S15	252	S13 not S14	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON.	2005/10/28 10:50
S16	1543	(diagonal with (write writing written erase erasing erasure erased program programming programmed))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/28 10:51
S17	925	(diagonal with (write writing written erase erasing erasure erased program programming programmed)) and memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/28 10:52
S18	888	(diagonal with (write writing written erase erasing erasure erased program programming programmed)) and memory	US-PGPUB; USPAT	OR	ON	2005/10/28 13:43
S19	197	(diagonal with (write writing written erase erasing erasure erased program programming programmed)) same memory	US-PGPUB; USPAT	OR	ON	2005/10/28 13:43

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	94	714/720.ccor.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/28 16:52
L2	4	714/720.ccor. and diagonal	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/28 16:54
L3	8	711/103.ccor. and diagonal	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/28 16:52
L4	23	365/201.ccor. and diagonal	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/28 16:54
S1	96	("5954831" "6212112" "6212112" "5644530" "5793249" "6665214" "6111801" "6918075" "5196722" "5399516" "5490264" "5656544" "6044016" "6301159" "5253363" "5644497" "4862418" "5783462" "5914512" "6233669" "6298429" "6850087" "5027353" "4847842" "4849974" "4849976" "5237646" "5241509" "5897350" "6091094" "6246087" "5271014" "4533865" "5337173" "6691066" "6617180" "5682349" "6108802" "5537632" "5469443" "5519713" "5528553" "3838681" "4517659" "5430456" "5600600" "6218807" "6218807" "4860142" "4875040").pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/28 16:51

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S9	10	S8 and S4	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/10/28 02:16